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2. (ONCE AMENDED) The driving method for a display apparatus as claimed in claim 1, wherein said clock signal used to drive said display panel is a source clock signal of said display apparatus.

3. (ONCE AMENDED) The driving method for a display apparatus as claimed in claim 1, wherein said clock used to drive said display panel continuously varies within a range of plus or minus 1 percent of a reference frequency.

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5. (ONCE AMENDED) The driving method for a display apparatus as claimed in claim 1, wherein a control of said clock signal used to drive said display panel is performed during a quiescent period.

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6. (ONCE AMENDED) A driving method for a display apparatus having a display panel, wherein peak noise output of the display panel is reduced by sequentially switching a clock signal, used to drive a display panel, between at least two frequencies .

7. (ONCE AMENDED) The driving method for a display apparatus as claimed in claim 6, wherein two frequencies lying within plus or minus 1 percent of a reference frequency are set for said clock signal used to drive said display panel.

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9. (ONCE AMENDED) The driving method for a display apparatus as claimed in claim 6, wherein a control of said clock signal used to drive said display panel is performed during a quiescent period.

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13. (ONCE AMENDED) The driving method for a display apparatus as claimed in claim 10, wherein a control of said clock signal used to drive said display panel is performed during a quiescent period.

14. (ONCE AMENDED) A display apparatus, comprising:  
a clock generating circuit;  
a drive waveform generating circuit generating a drive waveform by using a clock signal from said clock generating circuit having a continuously varying frequency; and  
a display panel displaying an image in accordance with said drive waveform, wherein

said drive waveform generating circuit drives said display panel by outputting the drive waveform having a varying frequency in accordance with said frequency varying clock signal.

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15. (ONCE AMENDED) The display apparatus as claimed in claim 14, wherein said clock generating circuit generates a source clock signal of said display apparatus.

16. (ONCE AMENDED) The display apparatus as claimed in claim 14, wherein said clock signal whose frequency varies continuously is within a range of plus or minus 1 percent of a reference frequency.

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18. (ONCE AMENDED) The display apparatus as claimed in claim 14, wherein during a quiescent period, said clock generating circuit performs a control of said clock signal used to drive said display panel.

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19. (ONCE AMENDED) A display apparatus, comprising;  
a clock generating circuit;  
a drive waveform generating circuit generating a drive waveform by using a clock signal from said clock generating circuit having a sequentially switched frequency switched between at least two frequencies; and

a display panel displaying an image in accordance with said drive waveform, wherein said drive waveform generating circuit drives said display panel by outputting the drive waveform having a switched frequency in accordance with said sequentially switched clock signal.

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20. (ONCE AMENDED) The display apparatus as claimed in claim 19, wherein said clock signal sequentially switched between two frequencies is within plus or minus 1 percent of a reference frequency.

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22. (ONCE AMENDED) The display apparatus as claimed in claim 19, wherein during a quiescent period, said clock generating circuit performs a control of said clock signal used to drive said display panel.

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23. (ONCE AMENDED) A display apparatus, comprising;  
a clock generating circuit;

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a drive waveform generating circuit generating a drive waveform by using a clock signal from said clock generating circuit having a sequentially switched frequency switched between at least two frequencies; and

a display panel displaying an image in accordance with said drive waveform, wherein said drive waveform generating circuit drives said display panel by sequentially switching an output drive waveform between drive waveforms corresponding to at least two frequencies.

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26. (ONCE AMENDED) The display apparatus as claimed in claim 23, wherein during a quiescent period, said clock generating circuit performs a control of said clock signal used to drive said display panel.

27. (NEW) A driving method for a display apparatus, comprising:  
continuously varying a frequency of a clock signal; and  
driving a display panel with said continuously varying frequency clock signal.

28. (NEW) The driving method for a display apparatus as claimed in claim 27, further comprising:  
using the continuously varying frequency clock signal as a source clock signal of said display apparatus.

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29. (NEW) The driving method for a display apparatus as claimed in claim 27, wherein the continuous varying of the frequency of the clock signal is within a range of plus or minus 1 percent of a reference frequency.

30. (NEW) The driving method for a display apparatus as claimed in claim 27, wherein said display apparatus is a plasma display apparatus.

31. (NEW) The driving method for a display apparatus as claimed in claim 27, further comprising:  
performing a control of said clock signal used to drive said display panel during a quiescent period.

32. (NEW) The driving method for a display apparatus as claimed in claim 27,

wherein said driving of the display panel reduces peak values of noise emitted by the display panel.

33. (NEW) The driving method for a display apparatus as claimed in claim 27, wherein said driving of the display panel spreads out frequencies of noise emitted by the display panel.

34. (NEW) A driving method for a display apparatus, comprising:  
sequentially switching a clock signal between at least two frequencies; and  
driving a display panel by with said sequentially switched clock signal to reduce peak values of noise emitted by the display panel.

35. (NEW) A driving method for a display apparatus, comprising:  
providing drive waveforms for a display panel corresponding to a plurality of frequencies;  
sequentially switching an output drive waveform between said drive waveforms  
corresponding to said plurality of frequencies; and  
driving said display panel by said drive waveforms corresponding to said plurality of frequencies.

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36. (NEW) A display apparatus including a display panel to display an image, comprising:  
a clock generating circuit to generate a clock signal having a continuously varying frequency; and  
a drive waveform generating circuit generating a drive waveform having a frequency varying in accordance with said frequency varying clock signal and driving the display panel in accordance with the generated drive waveform.

37. (NEW) A display apparatus including a display panel to display an image comprising:  
a clock generating circuit to generate a clock signal based on a spread-type clock oscillator; and  
a drive waveform generating circuit generating a drive waveform having a frequency varying in accordance with the generated clock signal and driving the display panel in